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Patent
PD-201151
Customer No. 020991

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Chen et al.

Serial No.: 10/068,047

Filed: February 5, 2002

For:

DUAL LAYER SIGNAL PROCESSING IN A LAYERED
MODULATION DIGITAL SIGNAL SYSTEM

Group Art Unit: 2631

Examiner: To Be Determined

I hereby certify that this paper (or fee) is being
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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, DC 20231

Dear Sir:

The references cited on the enclosed form PTO-1449 (copies of references enclosed) may be material to the examination of the above-identified patent application and are, therefore, submitted in compliance with the duty of disclosure defined in 37 CFR 1.56. It is respectfully requested that the references be considered and made of record in the application identified above.

This Information Disclosure Statement is not to be construed as a representation that a search has recently been made, that additional information material to the examination of this application does not exist, or that this citation constitutes prior art.

It is believed that no fee is due with this submission. If a fee is due, the Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment, to Deposit Account No. 50-0383 of Hughes Electronics Corporation, El Segundo, California.

Respectfully submitted,

By:

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PATENT APPLICATION

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Form PTO-1449	Serial Number 10/068,047	Docket PD- 201151
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Applicant Chen et al.	
(Use several sheets if necessary)	Filing Date February 5, 2002	Group

U.S. PATENT DOCUMENTS

		Document Number	Date	Name	Class	Sub Class
		4,039,961	08/02/1977	Ishio et al.		
				COPY OF PAPERS ORIGINALLY FILED		

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Name	Class	Sub Class

OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, etc.)

Examiner		Date Considered